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| Form 1449* | Atty. Docket No.: 884.229US1 | Serial No. Unknown | PRO S. 744 JC 525 U.S. 09/46/99 12/14/99 |
| | Applicant: Rajendran Nair et al. | | |
| | Filing Date: Herewith | Group: Unknown | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) | | | |

U. S. PATENT DOCUMENTS

| **Examiner Initial | Document Number | Date | Name | Class | Subclass | Filing Date If Appropriate |
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| JL | 5,173,835 | 12/22/1992 | Cornett, K.D., et al. | 257 | 310 | 10/15/91 |
| JR | 5,405,790 | 04/11/1995 | Rahim, I., et al. | 437 | 34 | 11/23/93 |
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| **Examiner Initial | Document Number | Date | Country | Class | Subclass | Translation Yes No |
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OTHER DOCUMENTS

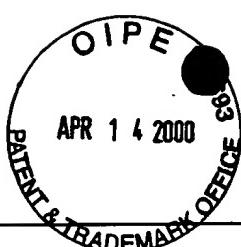
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Examiner Jung Kim Date Considered 7-3-01

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Sheet 1 of 1

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|---|----------------------------------|-----------------------|
| Form 1449* | Atty. Docket No.: 884.229US1 | Serial No. 09/460,742 |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) | Applicant: Rajendran Nair et al. | |
| | Filing Date: December 14, 1999 | Group: 2815 |

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| JK | Pierret, R.F., et al., "Field Effect Devices", <u>Volume IV of Modular Series on Solid State Devices</u> , 2nd Edition, Addison-Wesley Publishing Company, 47-59, (1990) |
| JTC | Weste, N.H., et al., "Principles of CMOS VLSI Design: A Systems Perspective", 2nd Edition, Addison-Wesley Publishing Company, 41-51 and 180-187, (1993) |

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